

**Amendments to the Specification**

Please replace the title with the following title.

~~Semiconductor Processing Methods of Forming a Plurality of Capacitors on a Substrate, Bit Line Contacts and Method of Forming Bit Line Contacts~~

At page 1, please replace the related patent data section with the following replacement paragraph.

**RELATED PATENT DATA**

This patent application is a divisional of U.S. Patent Application Serial No. 10/289,723, filed on November 6, 2002, which is a divisional of U.S. Patent Application Serial No. 10/121,538, filed on April 10, 2002, which is a continuation application of U.S. Patent Application Serial No. 09/651,816, filed on August 30, 2000, now U.S. Patent No. 6,395,613 B1, entitled "Semiconductor Processing Methods of Forming a Plurality of Capacitors on a Substrate, Bit Line Contacts and Method of Forming Bit Line Contacts", naming Werner Juengling as inventor, which is ~~This patent resulted from a continuation-in-part application of U.S. Patent Application Serial No. 09/036,701, filed on March 6, 1998, entitled "Semiconductor Processing Methods Of Forming Devices On A Substrate, Forming Device Arrays On A Substrate, Forming Conductive Lines On A Substrate, And Forming Capacitor Arrays On A Substrate, And Integrated Circuitry", naming Werner Juengling as inventor, and which is now~~ U.S. Patent No. 6,482,731 ~~U.S. Patent No. \_\_\_\_\_~~ which is a divisional application of U.S. Patent Application Serial No.

Priority Appl. No. 10/289,723

08/742,895, filed on November 1, 1996, having the same title and inventor and which is now U.S. Patent No. 5,998,256, the disclosure of which is incorporated by reference.